## In the Claims:

- 1 (Currently Amended) A system for recording data in a multi-board solid-state storage system, comprising:
  - i at least one main board that includes a processing system for enabling interaction with a host system;
  - ii at least one a plurality of memory boards separate from said main board, such that each said at least one memory board carries at least part of the storage system's primary solid-state components array used for data storage; and
  - iii for each said memory board, at least one secondary non-volatile memory device, located on said each memory board, and containing at least one faulty location record for said primary solid-state components array located on said each memory board;

wherein said primary solid-state components are non-volatile memory devices.

- 2 (Original) The system of claim 1, wherein said main board includes:
- a) at least a portion of said primary solid-state components array; and
- b) at least one respective secondary non-volatile memory device containing system information related to said main board.
- 3 (Canceled)
- 4 (Currently Amended) A method for recording system information in a multi-board solid state storage system that includes a main board and a plurality of memory boards separate from the main board, the main board including a processing

system for enabling interaction with a host system, the method comprising the steps of:



- i Providing primary non-volatile solid-state memory devices on each memory board of the multi-board solid state storage system;
- Placing a respective secondary non-volatile memory device onto each memory board of the multi-board solid state storage system; and
- iii For each <u>memory</u> board, recording faulty location records of said each board on said secondary non-volatile memory device thereof.
- 5 (Original) The method of claim 4, further comprising:
- iv performing direct actions selected from the group consisting of testing, adding, connecting and replacing said boards.
- 6 (Canceled)
- 7. (Original) The system of claim 1, wherein said primary solid-state components are Flash memory devices.
- 8. (Original) The method of claim 4, wherein said primary non-volatile solid-state memory devices are Flash memory devices.
- 9. (Original) A method for recording system information in a multi-board solid-state storage system, comprising the steps of:
  - i. placing a respective secondary non-volatile memory device onto each
    board of the multi-board solid-state storage system; and

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- ii. for each board, recording at least one faulty location record of said each board in each of at least two areas of said secondary non-volatile memory device thereof.
- 10. (Original) The method of claim 9, further comprising the step of:
- iii. for at least one board, subsequent to said recording, updating said at least one faulty location record of one of said at least two areas.
- 11. (Original) The method of claim 9, further comprising the step of:
- iii. for at least one board, subsequent to said recording, adding at least one additional faulty location record to one of said at least two areas.
- 12. (New) The system of claim 1, comprising at least three said memory boards.
  - 13. (New) The method of claim 4, further comprising the steps of:
  - iv. providing a primary non-volatile solid-state memory device on the main board of the multi-board solid state storage system;
  - v. placing a respective secondary non-volatile memory device onto the main board of the multi-board solid state storage system; and
  - vi. recording faulty location records of the main board on said secondary non-volatile memory device thereof.